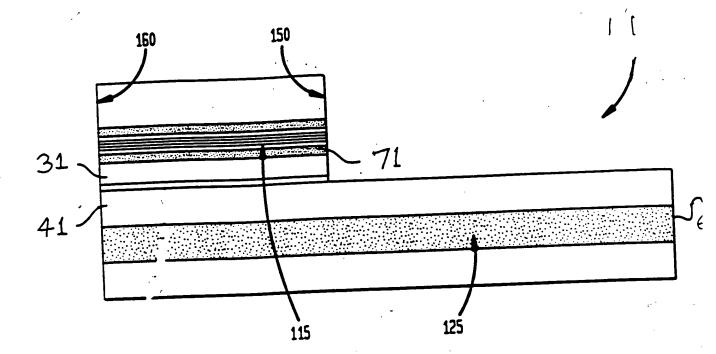


Docket No.: PUAS-0016
Title: TWIN VARIABLE BASED DESIGN FOR PHOTONIC INTEGRACIRCUITS
Inventor(s): Stephen R. Forrest, et al.
Sheet 2 of 4

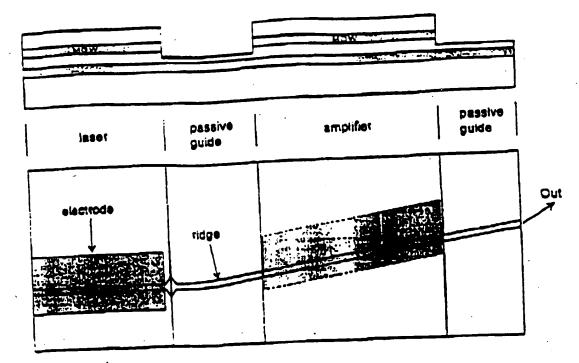


FIGUREZ

Docket No.: PUAS-0016
Title: TWO VAVEGUIDE BASED DESIGN FOR PHOTONIC INTEGRATED

CIRCUIT

Rephen R. Forrest, et al. Inventor(s, Sheet 3 of 4



active region

Figure 3

Docket No.: PUAS-0016
Title: TWIN WOYEGUIDE BASED DESIGN FOR PHOTONIC INTEGRATED CIRCUITS
Inventor(s): State on R. Forrest, et al.
Sheet 4
of 4

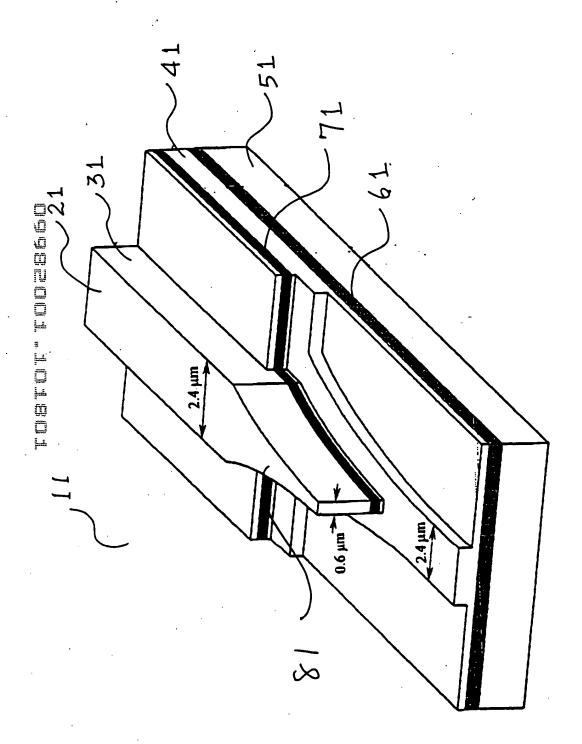


FIGURE , 4